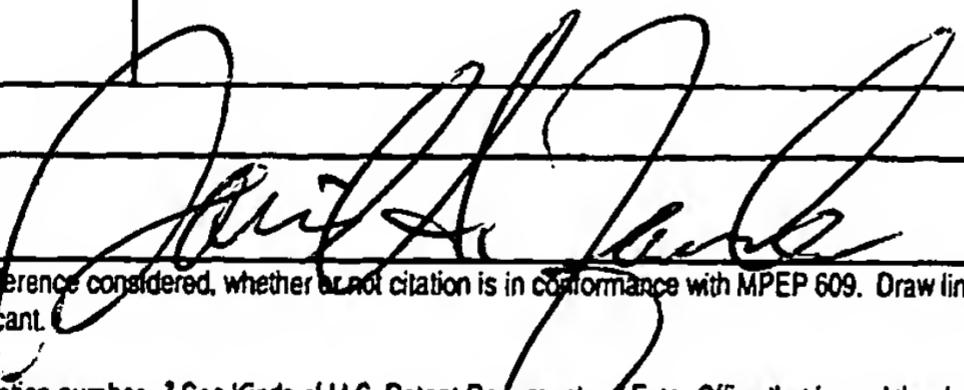


Substitute for form 1449A/PTO				<i>Complete if Known</i>	
				Application Number	
				Filing Date	Concurrently Herewith
				First Named Inventor	Robert F. Steinle
				Group Art Unit	2829
				Examiner Name	ZARNEKE, DAVID
Sheet	1	of	1	Attorney Docket Number	SC12711TP

U. S. PATENT DOCUMENTS					
Examiner Initials ¹	Cite No. ¹	Document Number Number -Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AA	AA	6,444,545 B1	09/03/02	Sadd et al.	
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
AD	AD	Cavins et al., "A Nitride-Oxide Blocking Layer for Scaled SONOS Non-Volatile Memory," Motorola, Inc., January 10, 2002, 5 pages.			
AE	AE	Cavins et al., "Integrated Stacked Gate Oxide and Interpoly Oxide," Motorola, Inc., November 1996, pp. 93-94.			

Examiner Signature		Date Considered	11/8/05
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